

S/N 10/810,035

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Albert S. Weiner	Examiner:	Thinh Nguyen
Serial No.:	10/810,035	Group Art Unit:	2818
Filed:	March 26, 2004	Docket:	2800.450US1
Customer No.:	76287	Confirmation No.:	8458
Title:	Non-volatile transistor memory array incorporating read-only elements with single mask set		

REQUEST FOR REFUND OF ISSUE FEE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant requests the large entity Issue Fee charged to applicant's Deposit Account 19-0743 on July 12, 2010 be refunded to Deposit Account 19-0743.


Applicant had previously paid the Issue Fee July 21, 2009. Applicant submitted on July 9, 2010 a Request to Apply Previously Paid Issue Fee (Copy Enclosed) in addition to Form PTOL-85 clearly marked with Total Fees Due as \$0 (Copy Enclosed).

Respectfully submitted,

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Date 9 September 2010

By


Robert E. Mates
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REM:CMG:fws

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 10 day of September, 2010.

Frederik Struve
Name


Signature